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PATENT
Attorney Docket No.: 018865-001740US
Client Reference No.:
17732.7226.001.001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

MO et al.

Application No.: 10/630,249

Filed: July 30, 2003

For: FIELD EFFECT TRANSISTOR
AND METHOD OF ITS
MANUFACTURE

Confirmation No.: 9390

Examiner: HA, Nathan W.

Art Unit: 2814

**SUPPLEMENTAL INFORMATION
DISCLOSURE STATEMENT UNDER
37 CFR §1.97 and §1.98**

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
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Sir:

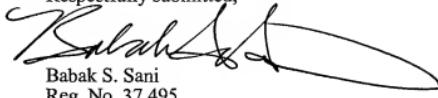
The references cited on attached form PTO/SB/08A are being called to the attention of the Examiner. Copies of the references [in compliance with the requirements of 37 CFR §1.98(a)(2)] are not enclosed. It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR §1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

This IDS is being filed before the mailing date of the final Office Action or
Notice of Allowance.

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Respectfully submitted,



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